

What is claimed is:

1. Apparatus for matching the impedance of a pair of RF sources coupled to a single electrode to the impedance of a plasma in a semiconductor substrate processing chamber, comprising:
 - a first sub-circuit for matching the impedance of a first RF signal generated by a first RF source to the impedance of the plasma; and
 - a second sub-circuit for matching the impedance of a second RF signal generated by a second RF source to the impedance of the plasma, the second sub-circuit connected to the first sub-circuit to form a common output that is coupled to the electrode.
2. The apparatus of claim 1, wherein the first and second sub-circuits each further comprise:
 - at least one fixed set of series components; and
 - at least one variable shunt component connected to ground.
3. The apparatus of claim 2, wherein a match tune space of the first and second RF sources is controllable by the shunt components.
4. The apparatus of claim 2, wherein a match tune space of the first and second RF sources is controllable by varying at least one of a first and a second frequency of a signal respectively generated by the first and second RF sources.
5. The apparatus of claim 1, wherein the first and second RF sources each have a 50 Ohm output impedance.
6. The apparatus of claim 1, wherein the first and second sub-circuits are fixed in a predetermined configuration prior to performing a particular process in the processing chamber.

7. The apparatus of claim 1, wherein the impedance of the first and second RF sources may be matched to the impedance of the processing chamber during processing by at least one of:

 varying at least one value of a component of the first and second sub-circuits during operation of the processing chamber; or

 varying the frequency of at least one of the first and the second RF sources.

8. The apparatus of claim 1, further comprising:

 an isolation sub-circuit for preventing power supplied from either of the first and second RF sources from being coupled to the other of the first and second RF sources.

9. Apparatus for matching the impedance of a pair of RF sources coupled to a single electrode to the impedance of a plasma in a semiconductor substrate processing chamber, comprising:

 a first sub-circuit for coupling to a first RF source and having a first set of fixed series components and a first variable shunt to ground; and

 a second sub-circuit for coupling to a second RF source and having a second set of fixed series components and a second variable shunt to ground, the second sub-circuit connected to the first sub-circuit to form a common output that is coupled to the electrode.

10. Apparatus for matching the impedance of a pair of RF sources coupled to a single electrode to the impedance of a plasma in a semiconductor substrate processing chamber, comprising:

 a processing chamber comprising at least a first electrode;

 a first RF source;

 a second RF source; and

 a dual frequency matching circuit, comprising:

 a first sub-circuit coupled to the first RF source; and

a second sub-circuit coupled to the second RF source and connected to the first sub-circuit to form a common output that is coupled to the first electrode.

11. The apparatus of claim 10, wherein the first and second sub-circuits each further comprise:

at least one fixed set of series components; and
at least one variable shunt component connected to ground.

12. The apparatus of claim 11, wherein a match tune space of the first and second RF sources is controllable by the shunt components.

13. The apparatus of claim 11, wherein a match tune space of the first and second RF sources is controllable by varying at least one of a first and a second frequency of a signal respectively generated by the first and second RF sources.

14. The apparatus of claim 10, wherein the first and second sub-circuits are fixed in a predetermined configuration prior to performing a particular process in the processing chamber.

15. The apparatus of claim 10, wherein the impedance of the first and second RF sources is matched to the impedance of the processing chamber during processing by at least one of:

varying at least one value of a component of the first and second sub-circuits during operation of the processing chamber; or

varying the frequency of at least one of the first and the second RF sources.

16. The apparatus of claim 10, wherein the dual frequency matching circuit further comprises:

an isolation sub-circuit for preventing power supplied from either of the first and second RF sources from being coupled to the other of the first and second RF sources.